

ABSTRACT

Power consumption required for charging and discharging a source signal line is reduced in an active matrix EL display device. A bipolar transistor (Bi1) has a base terminal B connected to an output terminal c1 of an operational amplifier (OP1), a 5 collector terminal C connected to a low power potential (GND), and an emitter terminal E connected to a resistor R2. A high power potential (VBH) is a potential in synchronization with a high power potential of a light emitting element. A potential of the output terminal c1 of the operational amplifier (OP1) is outputted as a buffer low power potential (VBL). The low power potential (VBL) corresponds to a potential 10 difference between the high power potential (VBH) and a high power potential (V1). Accordingly, the low power potential (VBL) can follow the high power potential (VBH), that is a high power potential of the light emitting element.